



PATENT
Attorney Docket No.: SAM-0126

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): In-Jung Lee, et al.
Serial No.: 09/633,366
Filing Date: August 7, 2000
Title: CAPACITOR AND FABRICATING METHOD THEREFOR

Examiner: Nguyen, C.
Group Art Unit: 2811

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AMENDMENT AFTER FINAL REJECTION

Sir:

This is in response to the final Office Action mailed on August 26, 2002. It is requested that the following amendments be entered and that the following remarks be considered.

Please amend the application as follows:

In the Claims

Please cancel claim 4.

Please amend the following claims:

- B1
sub
017
1. (Twice Amended) A semiconductor capacitor having a lower electrode, a dielectric layer and an upper electrode, wherein the upper electrode comprises a deposition structure including three layers including a doped polysilicon layer formed between a first undoped